

FSA2466

DATA / AUDIO Low-Voltage Dual DPDT Analog Switch

Features

Switch Type	DPDT (2x)
Input Type	Data / Audio Switch
Input Signal Range	0 to V_{CC}
V_{CC}	1.65 to 4.45 V
R_{ON}	2.5 Ω at 2.7 V
R_{FLAT}	0.8 Ω at 2.7 V
ESD	8 kV HBM
Bandwidth	245 MHz
C_{ON} at 240MHz	16 pF
C_{OFF} at 240MHz	6.0 pF
Features	Low I_{CCT}
Package	16- Lead UMLP 1.80 x 2.60 x 0.55 mm, 0.40 mm pitch
Top Mark	KA
Ordering Information	FSA2466UMX

Applications

- MP3 Portable Media Players
- Cellular Phones, Smartphones

Description

The FSA2466 is a dual Double-Pole, Double-Throw (DPDT) analog switch. The FSA2466 operates from a single 1.65 V to 4.45 V supply and features an ultra-low on resistance of 2 Ω at a +2.7 V supply and $T_A=25^\circ\text{C}$. This device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation.

FSA2466 features very low quiescent current even when the control voltage is lower than the V_{CC} supply. This allows mobile handset applications direct interface with the baseband processor general-purpose I/Os.

Related Resources

- For samples and questions, please contact:
Analog.Switch@fairchildsemi.com.
- FSA2466 Evaluation Board

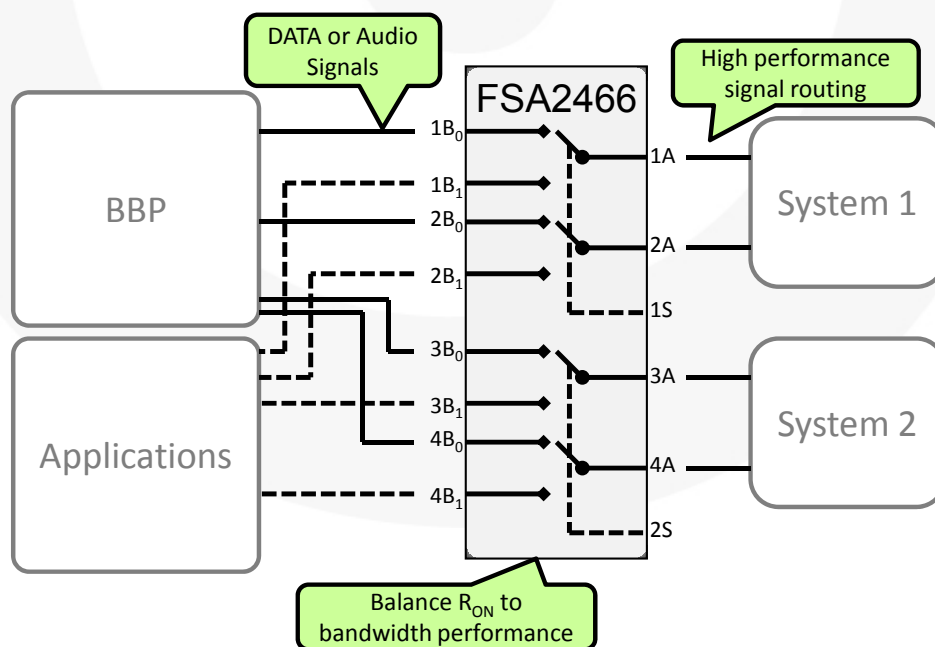


Figure 1. Typical Mobile Phone Application

Physical Dimensions

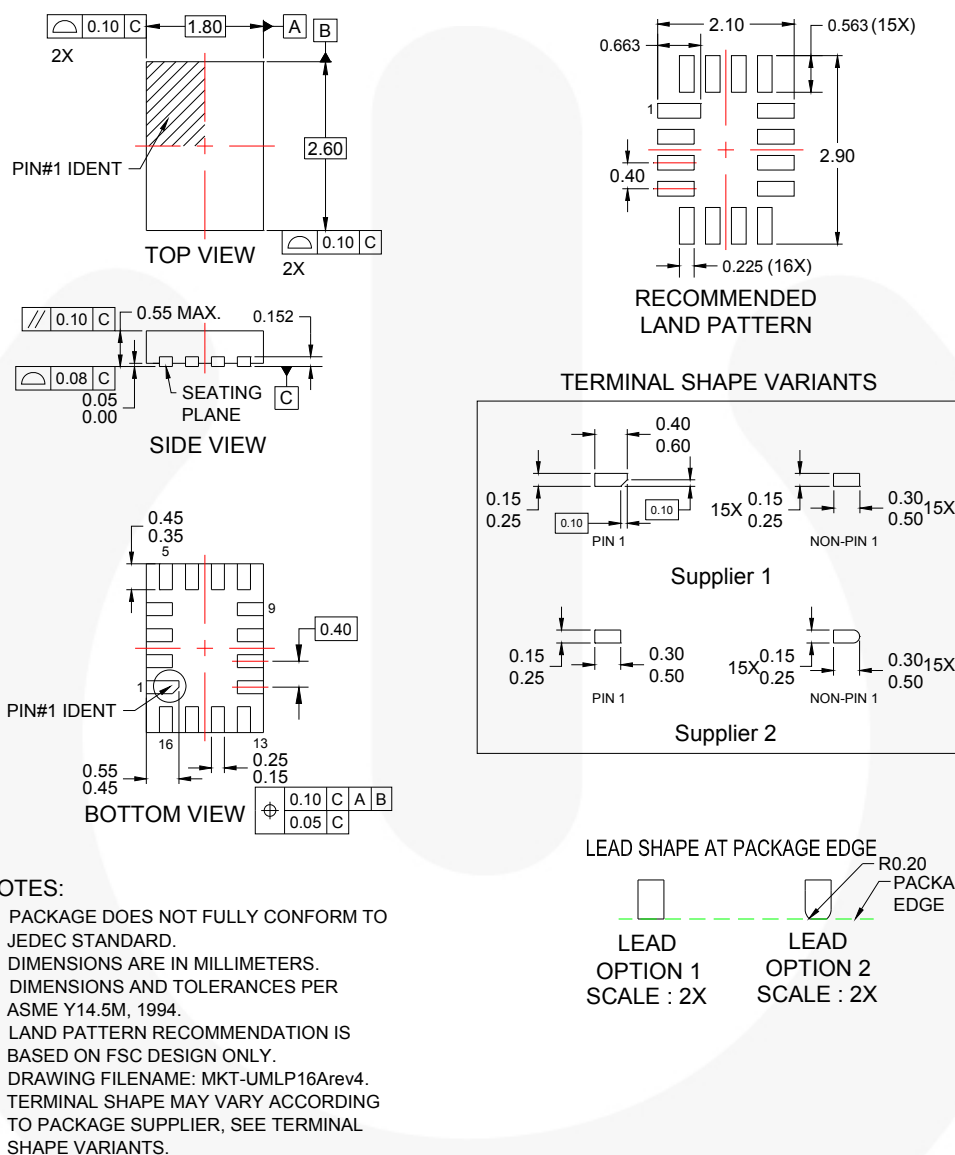


Figure 10. 16-Pin Ultrathin Molded Leadless Package (UMLP)

Order Number	Operating Temperature Range	Package Description	Packing Method
FSA2466UMX	-40 to 85°C	16-Terminal Ultrathin Molded Leadless Package	Tape & Reel





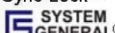
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